



HIGH RESOLUTION OFFSET GENERATOR HROG-10 OPERATING MANUAL

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Description



The HROG-10 is a high-resolution phase and frequency offset generator. The phase and frequency of the output signals are adjustable with respect to a 10 MHz user supplied reference. The output phase resolution of the generator is $2\pi/2^{32}$ radians or an output time step resolution of 0.024 fs. The output frequency resolution is 5 x 10⁻¹⁹. Both phase and frequency steps are phase continuous.

The instrument provides two sine-wave outputs and two pulse outputs. The sinewave outputs are buffered to provide greater than 80 dB of port-to-port and reverse isolation. The outputs are at a level of +13 dBm. The pulse outputs are derived from the sinewave outputs by dividing by a factor of 10.0E6. The pulse outputs can be synchronized to an external reference pulse to within 100 ns.

All instrument functions are displayed and controlled via the front panel LCD touch screen. Remote control of the instrument is possible through RS-232 communications. The HROG-10 comes in a stand-alone 2U rack mount enclosure.

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Safety and Preparation for Use



CAUTION!

Voltages capable of causing injury or death are present in this instrument. Use extreme caution whenever the instrument cover is removed.

Line Voltage

This instrument can be setup to operate on 100-120 or 220-240 VAC and a line frequency of 50 to 60 Hz. The setup voltage for this HROG-10 is specified on page 6. For conversion to a different line voltage please contact SDI.

Fuse

A 2.0 Ampere 250V slow-blow fuse is used for 100-120 or 220-240 VAC operation. Only replace fuses with the same type and specifications.

Line Cord

The HROG-10 has a detachable, three wire power cord for connection to a grounded power source. The enclosure of the unit is directly connected to the outlet ground to protect against electrical shock. Always use an outlet with a protective ground and do not disable this safety mechanism.

Service

Do not attempt to service or adjust the instrument unless another person, capable of providing first aid or resuscitation, is present.

Operation

To operate the unit, locate the AC power entry connector on the rear panel and connect the power cable. When power is applied to the unit, a green LED located on the front panel, labeled "ON", should light up.

Important!!!

The HROG-10 is a frequency and phase offset generator, therefore an external reference is required for proper operation. The external reference provided should be at 10 MHz +/-0.1Hz with a level of +7 to +15 dBm.

The Front Panel



ON The LED is on, when power is applied to unit.

- **DATA** The LED is on when data is being sent or received via the RS-232 port.
- **STATUS** The status LED is on when an error has occurred. View the instrument status via the PLL screen. The LED will turn off once the error condition is corrected or no longer present and the PLL status has been checked. If the HROG-10 is under RS-232 control use the *SRE to determine the error condition and the *CLS command to clear the status register and turn off the STATUS LED.
- **RS-232** DB-9 connector for serial communications. This is a dumb terminal RS-232 port. A null modem adapter is not required.
- **DISPLAY** The LCD touch screen is used to control the HROG-10 in local control mode.

REFERENCE INPUTS

- **10 MHz In** SMA input for the external 10 MHz reference. This input port has an impedance of 50 ohms. The external reference provided should be at 10 MHz +/- 0.1 Hz with a level of +7 to +15 dBm.
- **Signal LED** The 10 MHz signal LED will turn on when a 10 MHz reference is present at a level of +7 to +15 dBm.
- **PLL LED** The PLL lock LED will turn on when the HROG-10 is phase locked to an external reference.
- **1 PPS In** SMA input for an external one pulse per second signal (1 PPS) for synchronization. This input port has an impedance of 50 Ω. The 1 PPS signal should conform to TTL specifications and must not exceed +5.5 VDC. The voltage at this input must never be negative or the synthesizer will be damaged and warranty voided.
- **1 PPS LED** The 1 PPS indicator LED will flash when an external 1 PPS signal is present.

The Front Panel



OUTPUTS

- **10 MHz Out 1** SMA output number one providing the frequency and phase offset 10 MHz signal. This output signal has a level of +13 dBm.
- **Signal LED** The 10 MHz signal LED will turn on when the 10 MHz outputs are present.
- **10 MHz Out 2** SMA output number two providing the frequency and phase offset 10 MHz signal. This output signal has a level of +13 dBm.
- **1 PPS Out 1** SMA output number one providing the frequency and phase offset 1 PPS signal. This output signal has a level greater than 3.7 V into a 50 ohm load.
- **1 PPS LED** The 1 PPS LED will blink when the 1 PPS output is present.
- **1 PPS Out 2** SMA output number two providing the frequency and phase offset 1 PPS signal. This output signal has a level greater than 3.7 V into a 50 ohm load.

| The Back Panel | SD |
|---|----|
| AC POWER ENTRY MODULE The HROG-10 is configured to operate on: 100-120 VAC 220-240 VAC | |
| | |
| | |
| | |
| | |

RS-232 Port



RS-232 Communication Port

The HROG-10 functions are accessed through the RS-232 port located on the front panel. A standard serial cable with a DB-9 connector can be used to interface to the HROG-10. The user can input commands using a simple dumb terminal program on a remote computer or more sophisticated control can be used with software such as Labview.

On the front panel above the RS-232 connector there are three LEDs. The power LED labeled ON should be lit when power is applied. The second LED labeled DATA will light up only when data is being received or sent on the RS-232 port. This LED can be used to verify that the unit is communicating. The third LED is labeled status and is a hardware representation of the internal status flag. The status LED is on whenever an error has occurred. The user must query the unit to determine the source of error and then clear the error flag. When the error flag is cleared the LED will turn off.

Port Settings

On power-up the RS-232 port settings are: Baud rate 9600 8 Bits 1 Stop Bit No Parity.

Hardware handshaking is not used. The DB-9 connector pinout is described below.

| Pin | Function |
|-----|----------|
| 1 | NC |
| 2 | Data out |
| 3 | Data in |
| 4 | NC |
| 5 | GND |
| 6 | NC |
| 7 | NC |
| 8 | NC |
| 9 | NC |
| | |

Mechanical Tuning



Mechanical Tuning

Mechanical frequency tuning is available to adjust the frequency of the internal HROG-10 oscillator. **Only fully qualified service personnel should perform this procedure.** Frequency adjustments should be made with the unit having been powered on for at least 2 hours. **Caution must be taken to avoid shorting or accidentally touching a line voltage point.**

- 1. To adjust the frequency of the oscillator, remove the top cover of the HROG-10. The oscillator module is located at the right side of the instrument. The tuning access for the 10 MHz oscillator is located on the top side of the oscillator enclosure. A hermetic cover screw must be removed with a screwdriver to gain access to the tuning screw. A small flat blade-tuning tool is needed to make the adjustment.
- 2. Connect the external 10 MHz reference to the input labeled 10 MHz In. Make sure that the reference signal level is between +7dBm and +15 dBm. Program the HROG-10 to the nominal frequency offset that you want to use. For most applications you may enter 0 Hz for the frequency offset.
- 3. Enter the PLL screen to view the RF power levels and control voltages. The internal oscillator power level should be 13 ± 2 dBm. The reference signal power level should be between +7 dBm and +15 dBm. If the HROG-10 is phase locked to the external reference the LOCK voltage will be greater than 0.2 V. Adjust the mechanical tuning screw to achieve a lock condition. Continue adjusting the mechanical tuning screw until the PLL voltage displayed is at 0 volts. At this point the internal lock indicator LED should be on, the LOCK voltage should be greater than 0.2 V and the PLL voltage should be at 0.0 ± 0.2 V.
- **4.** Replace all hermetic covers when done adjusting the frequency of the oscillators. Replace the top cover of the HROG-10.

Note: The HROG-10 should be turned on for 2 hours prior to any mechanical frequency adjustment.

Specifications



| PARAMETER | CONDITIONS | MIN | ТҮР | MAX | UNITS |
|----------------------------------|----------------------------|-----|--------------------|--------|---------|
| Phase Resolution | | - | 2π/2 ³² | - | radians |
| Phase offset range | | - | infinite | - | - |
| Time offset resolution | 10 MHz External Reference | - | 0.024 | - | fs |
| Frequency Resolution | | - | 5 E-19 | - | - |
| Frequency Tuning Range | | - | +/- 1 E-7 | - | - |
| Mech. Tuning Range | | - | +/-1 E-6 | - | - |
| Int. Oscillator Aging | After 30 days of operation | - | 5 E-10 | - | Per day |
| 10 MHz Output Level | 50 Ohm Load | +10 | +13 | +15 | dBm |
| 1 PPS Output Level | 50 Ohm Load | 3.7 | 3.8 | 5.0 | V |
| 1 PPS Rise Time | 50 Ohm Load | - | 1.7 | 2 | ns |
| 1 PPS Fall Time | 50 Ohm Load | - | 1.2 | 2 | ns |
| Output Isolation | Channel to channel | - | 80 | - | dB |
| | Reverse | - | 80 | - | |
| Phase Noise ∠(f) | 10 Hz | - | -130 | -127 | dBc/Hz |
| Note: lower phase noise is | 100 Hz | - | -155 | -152 | |
| available. | 1 kHz | - | -165 | -162 | |
| | >10 kHz | - | -165 | -163 | |
| Allan Deviation $\sigma_y(\tau)$ | ∆f = 1.0 E-12 | | | | |
| | 1 s | - | 9 E-14 | 2 E-13 | |
| | 10 s | - | 9 E-15 | 3 E-14 | |
| | 100 s | - | 4 E-15 | 5 E-15 | |
| | 1000 s | - | 4 E-15 | 5 E-15 | |
| Allan Deviation $\sigma_y(\tau)$ | ∆f = 0 | | | | |
| | 1 s | - | 9 E-14 | 1 E-13 | |
| | 10 s | - | 9 E-15 | 2 E-14 | |
| | 100 s | - | 9 E-16 | 2 E-15 | |
| Spurious | | - | -110 | -100 | dBc |
| Harmonics | | - | -45 | -40 | dBc |

External Reference External 1 PPS AC Power 10.0 MHz ± 2.0E-8 400 ns min. pulse width 100–120 / 220–240 VAC +7 dBm to +15 dBm TTL Compatible Levels see page 6.

Rack-mount Enclosure Size: 3.5" X 19" X 17" Weight: 23 lbs

Main Screen



Main Screen The main screen displays the current frequency and phase offset of the HROG-10. The soft keys at the bottom of the screen display the five main functions that are available.

DISPLAYS

Frequency Offset

The frequency offset may be displayed in units of Hertz (Hz) or as a fractional number that is normalized to the reference frequency of 10.0 MHz. The frequency units may be changed by the following key sequence:

SET, UNITS, FREQ.

Phase Offset

The phase offset may be displayed in units of degrees (deg) or as a time offset in units of nanoseconds (ns). The phase units may be changed by the following key sequence:

SET, UNITS, PHASE.

STATUS

The status indicator is located in the upper right corner of the screen. Green indicates normal operation and red indicates an error condition.

MENU

| FREQ | Change frequency command. The FREQ key will bring up the Frequency Screen. |
|-------|--|
| PHASE | Change phase command. The PHASE key will bring up the Phase Screen. |
| TIME | Change time and date command. The TIME key will bring up the Time Screen. |
| SET | Change instrument settings. The SET key will bring up the Settings Screen. |
| HELP | Displays the Help Screen. |

Number Entry Screen



Number Screen The number entry screen is used to make numeric entries.

DISPLAYS

The current setting will be displayed across the top of the screen. The new entry is displayed in a number entry box.

SPECIAL KEYS

| Hz | Enter number in Hertz. | | |
|-------|--|--|--|
| uHz | Enter number in microHertz. | | |
| deg | Enter number in degrees. | | |
| mdeg | Enter number in millidegrees. | | |
| ns | Enter number in nanoseconds. | | |
| ps | Enter number in picoseconds. | | |
| ВК | Backspace. | | |
| ENTER | Enter new number and exit number menu. | | |
| ESC | Exit number menu discarding changes. | | |
| 0-9 | Numbers zero through nine. | | |
| | Decimal point. | | |
| - | Negative sign | | |
| + | Positive sign. | | |
| EXP | Exponential | | |

Frequency Screen



Frequency Screen The frequency screen displays the current frequency offset of the HROG-10. The soft keys at the bottom of the screen are used to set a new frequency offset.

DISPLAYS

Frequency Offset

The frequency offset may be displayed in units of Hertz (Hz) or as a fractional number that is normalized to the reference frequency of 10.0 MHz. The frequency units may be changed by the following key sequence:

EXIT, SET, UNITS, FREQ.

MENU

- **SET** Enter new frequency offset. The SET key will bring up the Number Entry Screen. The maximum frequency offset is +/- 2.0 Hz or 2.0 E-7. The frequency offset resolution is 5.0 E-19.
- **STEP** Enter a frequency step size. The STEP key will bring up the Number Entry Screen. The maximum step size is 2.0 E-7. Frequency step resolution is 5.0 E-19.
- **UP** Increase the frequency offset by the frequency step size.
- **DOWN** Decrease the frequency offset by the frequency step size.
- **EXIT** Exit to previous menu.

Phase Screen



Phase Screen The phase screen displays the current phase offset of the HROG-10. The soft keys at the bottom of the screen are used to set a new phase offset.

DISPLAYS

Phase Offset

The phase offset may be displayed in units of degrees (deg) or as a time offset in units of nanoseconds (ns). The phase units may be changed by the following key sequence:

EXIT, SET, UNITS, PHASE.

MENU

- **SET** Enter new phase offset. The SET key will bring up the Number Entry Screen. The difference between the new phase offset and the current phase offset must be less than 3600 degrees or 1000 ns. The phase slew rate is 9 degrees/second or 2.5 ns/second. The phase resolution is 8.4 E-8 degrees or 0.024 fs.
- **STEP** Enter a phase step size. The STEP key will bring up the Number Entry Screen. The phase offset step size is limited to 3600 degrees or 1000 ns. The phase step resolution is 8.4 E-8 degrees or 0.024 fs.
- **UP** Increase the phase offset by the phase step size.
- **DOWN** Decrease the phase offset by the phase step size.
- **EXIT** Exit to previous menu.

Settings Screen



Settings Screen The settings menu is used to access, view and edit instrument options.

MENU

| UNITS | Change phase or frequency units. |
|-------|----------------------------------|
|-------|----------------------------------|

PHASE Press the PHASE key to toggle phase units. Options are: Phase units in degrees Time units in nanoseconds

- FREQ Press the FREQ key to toggle frequency units. Options are: Frequency units in Hertz Fractional frequency with reference to 10.0 MHz
- **COMM** RS-232 options and control. The COMM key will bring up the Communications Screen.
- **PPS** 1 Pulse per second output options and control. The PPS key will bring up the 1 PPS Screen.
- **INST** Instrument setup and information. The INST key will bring up the Instrument Screen.
- **EXIT** Exit to previous menu.



Communications Screen The communications screen displays the current RS-232 serial port settings. The soft keys at the bottom of the screen are used to set new RS-232 settings, initiate RS-232 control of the instrument or test the serial port connection. The RS-232 port is setup to be controlled by a dumb terminal. A null modem adapter is not needed and should not be used. Hardware handshaking is not used. For additional pin-out information please refer to the RS232 port section on page 7 of this manual.

DISPLAYS

Current Baudrate and MODE settings.

MENU

| REM | Enter remote RS-232 control mode. |
|-------|---|
| MODE | Toggle through boot up communication options. Boot NORMAL Echo OFF: Normal start up* Boot NORMAL Echo ON: Normal start up** Boot RS-232 Echo OFF: Starts up in remote RS-232 control mode* Boot RS-232 Echo ON: Starts up in remote RS-232 control mode** Boot Concurrent Echo OFF: RS-232/Front-panel control mode *** Boot Concurrent Echo ON: RS-232/Front-panel control mode *** * Echo OFF: Does not echo back commands through RS-232 port. ** Echo ON: Sends back received commands through RS-232 port. *** The instrument can be controlled by the front panel and through the RS-232 port concurrently. |
| BAUD | Toggle through available baudrates. 9600 , 19200, 38400, 57600, 115200, 14400, 28800 |
| TEST | Used to test the RS-232 connection. Sends the following string through the serial port: "Testing Serial Port" "Hit Enter to Continue" |
| LOCAL | Return to local control and terminate remote RS-232 control session. (This button appears only in remote RS-232 control mode). |
| EXIT | Exit to previous menu. |
| | |

1 PPS Screen



1 PPS Screen The 1 PPS screen displays the current 1 PPS generator settings. The soft keys at the bottom of the screen are used to change the 1 PPS settings and synchronize to an external 1 PPS source.

DISPLAYS

- **Mode** The 1 PPS generator can be in 1 PPS mode generating an output one pulse per second signal, or in test mode generating a 100 Hz test signal.
- **PW** The pulse width of the one pulse per second output signal.

MENU

- **RST** Reset the 1 PPS counter, used for diagnostics only.
- **SYNC** Synchronize the output 1 PPS to an external 1 PPS signal. The time difference between the output 1 PPS and external 1 PPS should be less than 100 ns for a successful synchronization. A timeout error will occur if no external 1 PPS signal is present.
- SPW Toggle through the available pulse width settings of the 1 PPS signal. Pulse width options:

0.4 us, 1.6 us, 6.4 us, 25.6us, 51.2 us, 102.4 us, 204.8 us, 409.6 us

EXIT Exit to previous menu.

Instrument Screen



Instrument Screen The instrument screen is used to view or instrument configuration settings.

MENU

- **REG** View center frequency and the frequency registers, reset the phase counter and view reference frequency.
 - **RPHS** Reset the phase offset counter to zero.
 - **NVAL** View the frequency registers.
 - **REF** (Factory setting) View the frequency that is used as the external reference. The external reference frequency is not user selectable and must not be changed.
- **DISP** Change the contrast of the LCD display.
 - **UP** Increase the value.
 - **DOWN** Decrease the value.
 - **EXIT** Exit to previous menu.
- **PLL** View phase-lock-loop control voltages and levels. The PLL key brings up the PLL window.
- **EXIT** Exit to previous menu.

PLL Screen



PLL Screen The PLL Screen is used to view the current PLL voltages and RF power levels. Use this function to clear the status register and turn off the status LED. Note that the status LED will turn off only if the error condition has been resolved.

DISPLAYS

- **OSC** The power level of the internal oscillator. This level should be +13 dBm \pm 2 dB.
- **REF** The power level of the external reference. This level should be between +7 dBm and +15 dBm for proper operation.
- **LOCK** The lock indicator voltage. A voltage greater than 0.2 V indicates that the HROG-10 is phase locked to the external reference.
- **PLL** The tuning port voltage on the HROG-10 internal oscillator. For proper operation the tuning port voltage is \pm 5.0 V. If the voltage displayed is within 0.5 V of these limits the internal oscillator may need to be mechanically tuned. Please refer to the mechanical tuning section on page 8 of this manual for more information.
- **TEMP** The internal instrument temperature in degrees Celsius.

MENU

Exit Exit to previous menu.

ASCII Command Set



| Command Summary | Description | Page |
|-------------------|--------------------------------------|------|
| BAUD [baud] | Change baudrate | 20 |
| BAUD? | Query baud setting | 20 |
| DATE [mo/day/yr] | Change date | 21 |
| DATE? | Query date | 21 |
| FFOF [frac-freq] | Change fractional frequency offset | 22 |
| FFOF? | Query fractional frequency offset | 22 |
| FREQ [freq] | Change frequency offset | 23 |
| FREQ? | Query frequency offset | 23 |
| HELP | Basic help on ASCII command set | 24 |
| ID | Query model and serial number | 24 |
| LOCL | Return to local control | 25 |
| MODE [option] | Change RS-232 start up mode | 26 |
| MODE? | Query RS-232 start up mode | 26 |
| PHAS [phase] | Change phase offset | 27 |
| PHAS? | Query phase offset | 27 |
| PPSW [pwidth] | Change 1 PPS pulse width | 28 |
| PPSW? | Query 1 PPS pulse width | 28 |
| PLL? | Query phase lock loop status | 29 |
| SAVF | Frequency Offset saving option | 30 |
| SEDG | Select Clock Edge | 31 |
| SFFOF [ffstep] | Make fractional frequency step | 32 |
| SFFOF? | Query last fractional frequency step | 32 |
| SFREQ [fstep] | Make a frequency step | 33 |
| SFREQ? | Query last frequency step | 33 |
| SPHAS [pstep] | Make a phase step | 34 |
| SPHAS? | Query last phase step | 34 |
| STOFFS [tstep] | Change phase by a time step | 35 |
| STOFFS? [tstep] | Query last time step | 35 |
| SYNC | Synchronize to external 1 PPS | 36 |
| SYNC? | Query synchronization result | 36 |
| TEMP? | Query instrument temperature | 37 |
| TIME [hr:min:sec] | Change instrument time of day | 38 |
| TIME? | Query time of day | 38 |
| TOFFS [toffset] | Change time offset | 39 |
| TOFFS? | Query time offset | 39 |
| *RPHS | Reset phase offset counter | 40 |
| *SRE | Get status byte | 41 |
| *CLS | Clear status byte | 41 |
| | | |

BAUD, BAUD?



BAUD changes the baudrate of the synthesizer.

BAUD [baud]<cr>

BAUD has 1 parameter and is executed following the carriage return <cr>

baud baudrate valid values are:

96001920038400576001152001440028800

Example: BAUD 9600<cr>

Sets the baudrate of the synthesizer to 9600. <cr> is a carriage return.

BAUD? Queries the synthesizer baudrate.

BAUD?<cr>

BAUD? is executed following the carriage return <cr>.

The results of the query are in the following format. <lf> is a linefeed character.

BAUD? [baud]<cr><lf>

Example: BAUD?<cr>

Queries the baudrate setting of the synthesizer. <cr> is a carriage return. The function will return:

BAUD? 9600<cr><lf>

DATE, DATE?



DATE changes the date setting of the instrument.

DATE [month/day/year]<cr>

DATE has 3 parameters and is executed following the carriage return <cr>

momonth valid values are 1 - 12.dayday valid values are 1 - 31.yearyear valid values are 1971 - 2100.

Example: DATE 02/02/2015<cr>

Sets the date of the instrument to February 2, 2015. <cr> is a carriage return.

DATE? queries the instrument date setting.

DATE?<cr>

DATE? is executed following a carriage return.

The results of the query are in the following format. <lf> is a linefeed character.

DATE? [mo/day/year]<cr><lf>

Example: DATE?<cr>

Queries the date setting of the instrument. <cr> is a carriage return. The function will return:

DATE? 02/02/2015<cr><lf>





FFOF is used to make a fractional frequency offset. The HROG-10 output is offset in frequency from the external reference by the amount (frac_freq). To convert to a frequency offset in units of (Hz) use the following equation.

frequency offset (Hz) = frac_freq X reference frequency (Hz)

FFOF [frac_freq] <cr>

FFOF has one parameter and is executed following the carriage return <cr>

frac_freq range \pm (0 to 2.0 E-7) resolution is 5.0 E-19.

Example: FFOF 2.1E-10<cr>

The output of the HROG-10 will be offset from the reference by 2.1 E-10 or 0.0021 Hz for a 10 MHz reference. <cr> is a carriage return.

FFOF? queries the instrument fractional frequency offset.

FFOF?<cr>

FFOF? is executed following a carriage return.

The results of the query are in the following format. <lf> is a linefeed character.

FFOF? [frac_freq]<cr><lf>

Example: FFOF?<cr>

Queries the fractional frequency offset of the instrument. <cr> is a carriage return. The function will return:

FFOF? 2.1E-10<cr><lf>





FREQ is used to make a frequency offset. The HROG-10 output is offset in frequency from the external reference by the amount (freq).

FREQ [freq] <cr>

FREQ has one parameter and is executed following the carriage return <cr>

freq range \pm (0 to 2.0 Hz) resolution is 5.0 E-12 Hz.

Example: FREQ 0.001<cr>

The output of the HROG-10 will be offset from the reference by 1 mHz. <cr> is a carriage return.

FREQ? queries the instrument frequency offset.

FREQ?<cr>

FREQ? is executed following a carriage return.

The results of the query are in the following format. <lf> is a linefeed character.

FREQ? [freq]<cr><lf>

Example: FREQ?<cr>

Queries the frequency offset of the instrument. <cr> is a carriage return. The function will return:

FREQ? 0.001 Hz<cr><lf>

HELP, ID



HELP is used to display basic help on the ASCII command set.

HELP<cr>

HELP is executed following the carriage return <cr>

Example: HELP<cr>

ID is used to display the model, firmware version and serial number of the HROG-10.

ID<cr>

ID is executed following the carriage return <cr>

The results of the query are in the following format. <lf> is a linefeed character.

ID [model] [version] [serial number]<cr><lf>

Example: ID<cr>

Queries the description of the instrument. <cr> is a carriage return. The function will return:

ID HROG-10 Version 3.0 15FS12-03<cr><lf>



LOCL turns off the RS-232 communications and returns control to the LCD touch screen.

LOCL<cr>

LOCL is executed following the carriage return <cr>

MODE, MODE?



MODE is used to change the RS-232 communication startup options.

MODE [option] <cr>

MODE has one parameter and is executed following the carriage return <cr>

| option | valid values | s 0 – 5 |
|--------|--------------|------------------------------------|
| | value | Boot up mode |
| | 0 | Boot NORMAL with Echo OFF |
| | 1 | Boot NORMAL with Echo ON |
| | 2 | Boot RS-232 with Echo OFF |
| | 3 | Boot RS-232 with Echo ON |
| | 4 | Boot Concurrent Operation Echo OFF |
| | 5 | Boot Concurrent Operation Echo ON |

Example: MODE 3<cr>

The unit will boot up in RS-232 remote control mode and it will echo commands back through RS-232 port. <cr>> is a carriage return.

MODE? queries the current setting of the startup mode.

MODE? <cr>

MODE? is executed following a carriage return <cr>.

The results of the query are in the following format. <lf> is a linefeed character.

MODE? [option] <cr><lf>

Example: MODE? <cr>

Queries the current startup mode setting. <cr> is a carriage return.

The function will return: MODE? 3 <cr><lf>

PHAS, PHAS?



PHAS changes the phase of the HROG-10 with respect to the reference.

PHAS [phase]<cr>

PHAS has one parameter and is executed following the carriage return <cr>

phase value is in degrees the resolution is 8.4 E-8 deg.

The phase parameter keeps track of all phase changes that are implemented on the HROG-10. Issuing a **PHAS** command will cause the HROG-10 to offset the phase of the output signal relative to the 10 MHz reference signal to reach the new **phase** value. The power-on value of **phase** is zero and **phase** can be reset to zero without affecting the output phase by issuing the **RPHS** command.

Example: PHAS 360<cr>

Sets the output phase of the HROG-10 to +360 degrees from the phase = 0 condition. Note that if **phase** = 360 deg and we issue a **PHAS 360<cr>** command the output phase will not change because the synthesizer output is already at 360 deg.

PHAS? queries the instrument phase offset.

PHAS?<cr>

PHAS? is executed following a carriage return.

The results of the query are in the following format. If> is a linefeed character.

PHAS? [phase]<cr><lf>

Example: PHAS?<cr>

Queries the phase offset of the instrument. <cr> is a carriage return. The function will return:

PHAS? 360 deg<cr><lf>

PPSW, PPSW?



PPSW is used to change the pulse width of the 1 PPS signal.

PPSW [pwidth] <cr>

PPSW has one parameter and is executed following the carriage return <cr>

| valid values 0 – 7 | |
|--------------------|-------------------------------------|
| value | pulse width (µs) |
| 0 | 0.4 |
| 1 | 1.6 |
| 2 | 6.4 |
| 3 | 25.6 |
| 4 | 51.2 |
| 5 | 102.4 |
| 6 | 204.8 |
| 7 | 409.6 |
| | value 0 1 2 3 4 5 |

Example: PPSW 4<cr>

The 1 PPS output signal pulse width changes to 51.2 µs. <cr> is a carriage return.

PPSW? queries the 1 PPS output signal pulse width setting.

PPSW? <cr>

PPSW? is executed following a carriage return.

The results of the query are in the following format. <lf> is a linefeed character.

PPSW? [pwidth] [width in µs]<cr><lf>

Example: PPSW?<cr>

Queries the 1 PPS pulse width value. <cr> is a carriage return.

The function will return: **PPSW? 4 51.2 us<cr><lf>**



PLL? queries the critical levels and voltages in the main instrument control loop.

PLL?<cr>

PLL? is executed following the carriage return <cr>

OSC Power level of the internal oscillator. Valid range +11 to +15 dBm.

REF Power level of external reference signal. Valid range +7 to +15 dBm.

LOCK Voltage of the lock detector. Valid range is 0.2 to 0.35 V.

PLL Tuning port voltage on the oscillator. Valid range is +/- 5 V.

Note that if the tuning port voltage exceeds +/- 5 V the instrument will lose phase-lock to the external reference. This voltage can also be monitored and used to decide when the internal oscillator must be mechanically tuned to adjust for aging.

Example: PLL?<cr>

Queries the critical levels and voltages in the HROG-10 control loop. <cr> is a carriage return. <lf> is a linefeed character.

The function returns:

PLL? Osc: 12.0 dBm Ref: 15.0 dBm Lock: 0.3 V PLL: -0.2 V<cr><lf>



SAVF is used to enable or disable the storing of the frequency offset setting for the instrument in non-volatile memory. When the frequency offset is stored in non-volatile memory this value will be used upon power up of the instrument following a loss of power whether or not this power loss was intentional.

SAVF [state]<cr>

SAVF has 1 parameters and is executed following the carriage return <cr>

state state valid values are 0 or 1

0 – saving of the frequency offset setting to non-volatile memory is disabled. The instrument will power on to a frequency offset of 0 Hz.

1 – saving of the frequency to non-volatile memory is enabled. The instrument will power on to the last frequency offset setting received by the instrument.

Example: SAVF 1<cr>

The HROG-10 is set to save the last frequency offset value into non-volatile memory. This frequency offset value will be automatically loaded on startup or power up. <cr> is a carriage return.



SEDG is used to select the 1PPS output rising edge alignment with the internal 10 MHz reference clock.

SEDG [edge]<cr>

SEDGE has 1 parameters and is executed following the carriage return <cr>

edge edge valid values are 0 or 1

0 - 1PPS rising edge is aligned with the positive slope zero-crossing of the 10 MHz sine wave

1 - 1PPS rising edge is aligned with the negative slope zero-crossing of the 10 MHz sine wave.

Example: SEDG 1<cr>

The HROG-10 1PPS output 1 PPS rising edge will now be aligned with the negative slope zero-crossing of the output 10 MHz signal. <cr>> is a carriage return.

SFFOF, SFFOF?



SFFOF is used to make a fractional frequency step. The HROG-10 output frequency is changed by the amount specified by **ffstep**.

SFFOF [ffstep] <cr>

SFFOF has one parameter and is executed following the carriage return <cr>

ffstep range \pm (0 to 2.0 E-7) resolution is 5.0 E-19.

Example: SFFOF 1.0E-14<cr>

The output of the HROG-10 will change in frequency by 1.0 E-14. <cr> is a carriage return.

SFFOF? queries the last fractional frequency step.

SFFOF? <cr>

SFFOF? executed following a carriage return.

The query returns the last frequency step made by the instrument. It does not return the frequency offset between the HROG-10 and the reference input. To query the frequency offset use the **FFOF?** or **FREQ?** commands.

The results of the query are in the following format. If> is a linefeed character.

SFFOF? [ffstep]<cr><lf>

Example: SFFOF?<cr>

Queries the last frequency step. <cr> is a carriage return. The function will return:

SFFOF? 1E-14<cr><lf>

SFREQ, SFREQ?



SFREQ is used to make a frequency step. The HROG-10 output frequency is changed by the amount specified by **fstep**.

SFREQ [fstep] <cr>

SFREQ has one parameter and is executed following the carriage return <cr>

fstep range \pm (0 Hz to 2.0 Hz) resolution is 5.0 E-12 Hz.

Example: SFREQ 0.001<cr>

The output of the HROG-10 will change in frequency by 0.001 Hz. <cr> is a carriage return.

SFREQ? queries the last frequency step.

SFREQ? <cr>

SFREQ? executed following a carriage return.

The query returns the last frequency step made by the instrument. It does not return the frequency offset between the HROG-10 and the reference input. To query the frequency offset use the **FFOF?** or **FREQ?** commands.

The results of the query are in the following format.

SFREQ? [fstep] Hz<cr><lf>

Example: SFREQ?<cr>

Queries the last frequency step. <cr> is a carriage return. The function will return:

SFREQ? 0.001 Hz<cr><lf>

SPHAS, SPHAS?



SPHAS changes the phase of the HROG-10 output by the amount specified by pstep.

SPHAS [pstep]<cr>

SPHAS has one parameter and is executed following the carriage return <cr>

 pstep valid range is ± (0 to 3600 deg) resolution is 8.4 E-8 deg. The output of the HROG-10 will change in phase by the amount of pstep. The phase slew rate is 9 deg/s, therefore it takes 10 s to execute a 90 degree phase step.

Example: SPHAS 10<cr>

Steps the output phase of the HROG-10 by +10 degrees.

SPHAS? queries the last phase step executed by the instrument.

SPHAS?<cr>

SPHAS? is executed following a carriage return.

The results of the query are in the following format. If> is a linefeed character.

SPHAS? [pstep] deg<cr><lf>

Example: SPHAS?<cr>

Queries the last phase step executed by the instrument. <cr> is a carriage return. The function will return:

SPHAS? 9.99999999069 deg<cr><lf>

STOFFS, STOFFS?



STOFFS changes the time offset of the HROG-10 output by the amount specified by **tstep**.

STOFFS [tstep]<cr>

STOFFS has one parameter and is executed following the carriage return <cr>

tstepvalid range is ± (0 to 1000ns) resolution is 2.4 E-5 ns.The output of the HROG-10 will change in time offset by the amount of
tstep. The phase slew rate is 2.5 ns/s, therefore it takes 10 s to execute a
25 ns time step.

Example: STOFFS 10.0<cr>

Steps the output time offset of the HROG-10 by +10 ns.

STOFFS? queries the instrument for the last time step.

STOFFS? <cr>

STOFFS? is executed following a carriage return.

The results of the query are in the following format. If> is a linefeed character.

STOFFS? [tstep] ns<cr><lf>

Example: STOFFS?<cr>

Queries the unit for the last time step. <cr> is a carriage return. The function will return:

STOFFS? 9.9999996275 ns<cr><lf>

SYNC, SYNC?



SYNC synchronizes the output 1 PPS signal to an external 1 PPS signal to within +/- 50 ns. If the external 1 PPS signal is not present the synchronization procedure will abort. Anytime this command is executed the 1 PPS time tag is lost and the phase counter should be reset because the phase relation between the reference and internal oscillator is also lost. The following procedures should be executed following a **SYNC** command.

TIME Sets the time of day and also time tags the 1 PPS signal.

*RPHS Resets the phase counter to 0.

SYNC<cr>

SYNC is executed following the carriage return <cr>.

Example: SYNC<cr>

SYNC? queries the synchronization result.

Function will return:

| SYNC? 0 TIMEOUT <cr><lf></lf></cr> | Synchronization did not occur. Check external |
|------------------------------------|---|
| | 1 PPS. |

SYNC? 1 OK<cr><lf> Synchronization successful.

Example: SYNC?<cr>

SYNC? 1 OK<cr><lf>

Synchronization was successful.

TEMP?



TEMP? Queries the system temperature.

TEMP?<cr>

TEMP? is executed following a carriage return,

Example: TEMP?<cr>

Returns

TEMP? 40.1 C<cr><lf>

TIME, TIME?



TIME changes the time of day of the instrument.

TIME [hr:min:sec] <cr>

TIME has 3 parameters and is executed following the carriage return <cr>

hr Hour valid range 0 – 23

min Minutes valid range 0 – 59

sec Seconds valid range 0 – 59

Example: TIME 12:01:00<cr>

Sets the time of day to 12 hours, 1 minute and 0 seconds. <cr> is a carriage return.

TIME? Queries the system time of day.

TIME?<cr><lf>

TIME? is executed following a carriage return,

Example: TIME?<cr>

Returns

TIME? 12:01:31<cr><lf>

TOFFS, TOFFS?



TOFFS changes the time offset of the HROG-10 output to achieve the total time offset specified by **toffset**.

TOFFS [toffset]<cr>

TOFFS has one parameter and is executed following the carriage return <cr>

toffset time offset in ns, the resolution is 2.4 E-5 ns.

The time offset parameter keeps track of all time offset changes that are implemented on the HROG-10. Issuing a **TOFFS** command will cause the HROG-10 to change the time offset of the output signal relative to the 10 MHz reference signal to reach the new **toffset** value. The power-on value of **toffset** is zero and **toffset** can be reset to zero without affecting the time offset of the output signal by issuing the ***RPHS** command.

Example: TOFFS 100.0<cr>

Sets the output time offset of the HROG-10 to +100 ns from the time offset = 0 condition. Note that if **toffset** = 100 ns and we issue a **TOFFS 100.0<cr>** command the output time offset will not change because the synthesizer output is already at 100ns offset.

TOFFS? queries the instrument for the last time step.

TOFFS? <cr>

TOFFS? is executed following a carriage return.

The results of the query are in the following format. If> is a linefeed character.

TOFFS? [toffset] ns<cr><lf>

Example: TOFFS?<cr>

Queries the unit for the time offset relative to the reference. <cr> is a carriage return. The function will return:

TOFFS? 100.0ns<cr><lf>



***RPHS** sets the phase counter and time offset counter to zero. This function does not change the HROG-10 output signal.

*RPHS<cr>

*RPHS is executed following the carriage return <cr>

Example: *RPHS<cr>

Sets the phase and time offset counters = 0. <cr> is a carriage return.

*SRE, *CLS



*SRE queries the synthesizers for the value of the status register.

*SRE<cr>

*SRE is executed following a carriage return.

The results of the query are in the following format.

SRE [status]<cr><lf>

status is an 8-bit decimal value that contains the sum of the error conditions. status is 0 when there are no errors.

| External reference error 1 | |
|-----------------------------|---|
| Internal oscillator error 2 | |
| PLL Lock error 4 | |
| Tuning voltage error 8 | |
| Invalid parameter 16 | |
| Invalid command 32 | |
| Reserved 1 64 | |
| Reserved 2 128 | B |

Example: *SRE<cr> query status

The results of the status query are:

*SRE 10<cr><lf> the return value is the sum of 8 and 2 which indicates a tuning voltage error and an internal oscillator error. <lf> is a linefeed character.

***CLS** clears the status register and turns off the status LED.

*CLS<cr>

*CLS is executed following a carriage return.

Troubleshooting



The HROG-10 needs up to 30 minutes to warmup after power is applied to the unit. After this warmup period, the power on LED and the 10 MHz output signal LED should be on. The 1 PPS LED should be blinking at a 1 Hz rate. The external 10 MHz reference with a level of +7 dBm to +15 dBm should be connected to the instruments 10 MHz In SMA input. Once the external reference is applied the external reference signal LED will turn on and the HROG-10 will automatically lock to this external reference. Once the PLL is locked the PLL lock LED will turn on. The status LED will remain on until the PLL Screen is invoked and the PLL status is viewed. The external 1 PPS LED should blink when an external 1 PPS signal is present.

Problems

Unit does not turn on.

Check power cord, fuses and make sure on/off switch is in on position.

Unit does not respond to keypad

Make sure unit is in local control mode and not in RS-232 mode.

External reference signal LED is off

Check that the external 10 MHz signal is present and that the level is between +7 and +15 dBm.

PLL Lock LED is off or blinking

Check that the external 10 MHz signal is present. Check that the frequency of the external reference is 10.0 MHz +/- 0.1Hz and that the level is between +7 and +15 dBm.

Check that the HROG-10 output is present.

Check PLL status using the PLL Screen function.

OSC level should read +13 dBm +/- 2 dB REF level should be between +7 dBm and +15 dBm. LOCK voltage should be greater than 0.2 V PLL voltage should be between +4.5V and -4.5V The unit temperature should not exceed +50C.

If the unit remains unlocked the oscillator may require mechanical tuning. Refer to the Mechanical Tuning section on page 8.

Troubleshooting



10 MHz output signal LED is not on

Send unit to SpectraDynamics, Inc. for repair.

1 PPS LED is on all the time

The 1 PPS generator may be in TEST mode. Activate the PPS Screen and make sure that the PPS mode is set to 1 PPS and not TEST.

STATUS LED is on

Activate the PLL Screen and check the PLL status and make sure that the external reference is adequate. If the RS-232 control mode is being used use the *SRE command to read the status register and the *CLS command to clear the status register.

RS-232 communications failed

Check that the correct RS-232 cable is being used.

Do not use a null modem adapter.

Check baud rate of the HROG-10 and set the controller to the same baud rate.

Use the TEST function to test the serial connection.

The DATA LED should flash when data is being received by the HROG-10. Make sure that the HROG-10 is in remote control mode and not in local control mode.

If any error condition persists please contact technical support.

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Warranty



The HROG-10 is warranted to be free of defects under normal operating conditions, as specified, for one year from date of original shipment from SpectraDynamics, Inc (SDI). SDI's obligation and liability under this warranty is expressly limited to repairing or replacing, at SDI's option, any product not meeting the said specifications. This warranty shall be in effect for one (1) year from the date an HROG-10 is sold by SDI. SDI makes no other warranty, express or implied, and makes no warranty of the fitness for any particular purpose. SDI's obligation under this warranty shall not include any transportation charges or costs of installation or any liability for direct, indirect, or consequential damages or delay. Any improper use, operation beyond capacity, substitution of parts not approved by SDI, or any alteration or repair by others in such manner as in SDI's reasonable judgment affects the product materially and adversely shall void this warranty. No employee or representative of SDI is authorized to change this warranty in any way or grant any other warranty.

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